

SWITCHMODE POWER SUPPLY HANDBOOK

KEITH
BILLINGS



Other McGraw-Hill Reference Books of Interest

Handbooks

Avalone and Baumeister • STANDARD HANDBOOK FOR MECHANICAL ENGINEERS
Beeman • INDUSTRIAL POWER SYSTEMS HANDBOOK
Coombs • BASIC ELECTRONIC INSTRUMENT HANDBOOK
Coombs • PRINTED CIRCUITS HANDBOOK
Croft and Summers • AMERICAN ELECTRICIANS' HANDBOOK
Di Giacomo • VLSI HANDBOOK
Fink and Beaty • STANDARD HANDBOOK FOR ELECTRICAL ENGINEERS
Fink and Christiansen • ELECTRONICS ENGINEERS' HANDBOOK
Harper • HANDBOOK OF ELECTRONIC SYSTEMS DESIGN
Harper • HANDBOOK OF THICK FILM HYBRID MICROELECTRONICS
Harper • HANDBOOK OF WIRING, CABLING, AND INTERCONNECTING FOR ELECTRONICS
Hicks • STANDARD HANDBOOK OF ENGINEERING CALCULATIONS
Inglis • ELECTRONIC COMMUNICATIONS HANDBOOK
Johnson and Jasik • ANTENNA ENGINEERING HANDBOOK
Juran • QUALITY CONTROL HANDBOOK
Kaufman and Seidman • HANDBOOK FOR ELECTRONICS ENGINEERING TECHNICIANS
Kaufman and Seidman • HANDBOOK OF ELECTRONICS CALCULATIONS
Kurtz • HANDBOOK OF ENGINEERING ECONOMICS
Perry • ENGINEERING MANUAL
Stout • HANDBOOK OF MICROPROCESSOR DESIGN AND APPLICATIONS
Stout and Kaufman • HANDBOOK OF MICROCIRCUIT DESIGN AND APPLICATION
Stout and Kaufman • HANDBOOK OF OPERATIONAL AMPLIFIER DESIGN
Tuma • ENGINEERING MATHEMATICS HANDBOOK
Williams • DESIGNER'S HANDBOOK OF INTEGRATED CIRCUITS
Williams and Taylor • ELECTRONIC FILTER DESIGN HANDBOOK

Power Electronics Books

Antognetti • POWER INTEGRATED CIRCUITS
Chryssis • HIGH-FREQUENCY SWITCHING POWER SUPPLIES
Flanagan • HANDBOOK OF TRANSFORMER APPLICATIONS
Grossner • TRANSFORMERS IN ELECTRONIC CIRCUITS
Mitchell • DC/DC SWITCHING REGULATOR ANALYSIS
Rombaut • POWER ELECTRONIC CONVERTERS: AC/AC
Seguier • POWER ELECTRONIC CONVERTERS: AC/DC CONVERSIONS

Encyclopedias

CONCISE ENCYCLOPEDIA OF SCIENCE AND TECHNOLOGY
ENCYCLOPEDIA OF ELECTRONICS AND COMPUTERS
ENCYCLOPEDIA OF ENGINEERING

Dictionaries

DICTIONARY OF COMPUTERS
DICTIONARY OF ELECTRICAL AND ELECTRONIC ENGINEERING
DICTIONARY OF ENGINEERING
DICTIONARY OF SCIENTIFIC AND TECHNICAL TERMS
Markus • ELECTRONICS DICTIONARY

For more information about other McGraw-Hill materials,
call 1-800-2-MCGRAW in the United States. In other
countries, call your nearest McGraw-Hill office.

SWITCHMODE POWER SUPPLY HANDBOOK

Keith H. Billings, C.Eng., M.I.E.E.

Vice President, Engineering

Hammond Manufacturing Company, Ltd.

McGraw-Hill, Inc.

New York St. Louis San Francisco Auckland Bogotá
Caracas Lisbon London Madrid Mexico City Milan
Montreal New Delhi San Juan Singapore
Sydney Tokyo Toronto

To Diana
For more than four years of
quiet encouragement

Library of Congress Cataloging-in-Publication Data

Billings, Keith H.

Switchmode power supply handbook / Keith H. Billings.

p. cm.

ISBN 0-07-005330-8

1. Electronic apparatus and appliances—Power supply—Handbooks.
manuals, etc. I. Title.

TK7881.15.B55 1989

621.381'044—dc19

88-32774

CIP

Copyright © 1989 by McGraw-Hill, Inc. All rights reserved.
Printed in the United States of America. Except as permitted
under the United States Copyright Act of 1976, no part of this
publication may be reproduced or distributed in any form or by
any means, or stored in a data base or retrieval system, without
the prior written permission of the publisher.

567890 WHT WHT 909876543

ISBN 0-07-005330-8

The editors for this book were Daniel A. Gonneau and Susan Thomas,
the designer was Naomi Auerbach, and the production
supervisor was Dianne L. Walber. It was set in Times Roman
by the McGraw-Hill Publishing Company, Professional and Reference
Division Composition Unit.

Information contained in this work has been obtained by
McGraw-Hill, Inc., from sources believed to be reliable. However,
neither McGraw-Hill nor its authors guarantees the accuracy or
completeness of any information published herein and neither
McGraw-Hill nor its authors shall be responsible for any errors,
omissions, or damages arising out of use of this information. This
work is published with the understanding that McGraw-Hill and
its authors are supplying information but are not attempting to
render engineering or other professional services. If such services
are required, the assistance of an appropriate professional should
be sought.

For more information about other McGraw-Hill materials,
call 1-800-2-MCGRAW in the United States. In other
countries, call your nearest McGraw-Hill office.

TAB BOOKS Inc. offers software for sale. For information and a catalog, please
contact TAB Software Department, Blue Ridge Summit, PA 17294-0850.

CONTENTS

Preface xvi

Acknowledgments xviii

Units, Symbols, Dimensions, and Abbreviations Used in this Book xix

PART 1 FUNCTIONS AND REQUIREMENTS COMMON TO MOST DIRECT-OFF-LINE SWITCHMODE POWER SUPPLIES

- 1. COMMON REQUIREMENTS: AN OVERVIEW** 1.3
- 1.1 Introduction. 1.2 Input Transient Voltage Protection.
1.3 Electromagnetic Compatibility. 1.4 Differential-Mode Noise.
1.5 Common-Mode Noise. 1.6 Faraday Screens. 1.7 Input Fuse
Selection. 1.8 Line Rectification and Capacitor Input Filters.
1.9 Inrush Limiting. 1.10 Start-Up Methods. 1.11 Soft Start.
1.12 Start-Up Overvoltage Prevention. 1.13 Output Overvoltage
Protection. 1.14 Output Undervoltage Protection. 1.15 Overload
Protection (Input Power Limiting). 1.16 Output Current Limiting.
1.17 Base Drive Requirements for High-Voltage Bipolar Transistors.
1.18 Proportional Drive Circuits. 1.19 Antisaturation Techniques.
1.20 Snubber Networks. 1.21 Cross Conduction. 1.22 Output
Filtering, Common-Mode Noise, and Input-To-Output Isolation.
1.23 Power Failure and Power Good Signals. 1.24 Power Good
Signals. 1.25 Dual Input Voltage Operation. 1.26 Power Supply Hold-
up Time. 1.27 Synchronization. 1.28 External Inhibit. 1.29 Forced
Current Sharing. 1.30 Remote Sensing. 1.31 P-Terminal Link.
1.32 Low-Voltage Cutout. 1.33 Voltage and Current Limit
Adjustments. 1.34 Input Safety Requirements.
- 2. AC POWERLINE SURGE PROTECTION** 1.17
- 2.1 Introduction. 2.2 Location Categories. 2.3 Likely Rate of Surge
Occurrences. 2.4 Surge Voltage Waveforms. 2.5 Transient
Suppression Devices. 2.6 Metal Oxide Varistors (MOVs, Voltage-
Dependent Resistors). 2.7 Transient Protection Diodes. 2.8 Gas-filled
Surge Arresters. 2.9 Line Filter, Transient Suppressor Combinations.
2.10 Category A Transient Suppression Filters. 2.11 Category B
Transient Suppression Filters. 2.12 A Case for Full Transient
Protection. 2.13 The Cause of "Ground Return Voltage Bump" Stress.
2.14 Problems.
- 3. ELECTROMAGNETIC INTERFERENCE (EMI)
IN SWITCHMODE POWER SUPPLIES** 1.31
- 3.1 Introduction. 3.2 EMI/RFI Propagation Modes. 3.3 Powerline
Conducted-Mode Interference. 3.4 Safety Regulations (Ground Return
Currents). 3.5 Powerline Filters. 3.6 Suppressing EMI at Source.

3.7 Example. 3.8 Line Impedance Stabilization Network, (LISN). 3.9 Line Filter Design. 3.10 Common-Mode Line-Filter Inductors. 3.11 Design Example For Common-Mode Line-Filter Inductors. 3.12 Series-Mode Inductors. 3.13 Problems.	
4. FARADAY SCREENS	1.43
4.1 Introduction. 4.2 Faraday Screens as Applied to Switching Devices. 4.3 Transformer Faraday Screens and Safety Screens. 4.4 Faraday Screens on Output Components. 4.5 Reducing Radiated EMI in Gapped Transformer Cores. 4.6 Problems.	
5. FUSE SELECTION	1.49
5.1 Introduction. 5.2 Fuse Parameters. 5.3 Types of Fuses. 5.4 Selecting Fuses. 5.5 SCR Crowbar Fuses. 5.6 Transformer Input Fuses. 5.7 Problems.	
6. LINE RECTIFICATION AND CAPACITOR INPUT FILTERS FOR "DIRECT-OFF-LINE" SWITCHMODE POWER SUPPLIES	1.54
6.1 Introduction. 6.2 Typical Dual-Voltage Capacitor Input Filter Circuits. 6.3 Effective Series Resistance R_s . 6.4 Constant-Power Load. 6.5 Constant-Current Load. 6.6 Rectifier and Capacitor Waveforms. 6.7 Input Current, Capacitor Ripple, and Peak Currents. 6.8 Effective Input Current I_e and Power Factor. 6.9 Selecting Inrush- Limiting Resistance. 6.10 Resistance Factor R_{sf} . 6.11 Design Example. 6.12 DC Output Voltage and Regulation for Rectifier Capacitor Input Filters. 6.13 Example of Rectifier Capacitor Input Filter DC Output Voltage Calculation. 6.14 Selecting Reservoir and/or Filter Capacitor Size. 6.15 Selecting Input Fuse Ratings. 6.16 Power Factor and Efficiency Measurements. 6.17 Problems.	
7. INRUSH CONTROL	1.73
7.1 Introduction. 7.2 Series Resistors. 7.3 Thermistor Inrush Limiting. 7.4 Active Limiting Circuits (Triac Start Circuit). 7.5 Problems.	
8. START-UP METHODS	1.76
8.1 Introduction. 8.2 Dissipative (Passive) Start Circuit. 8.3 Transistor (Active) Start Circuits. 8.4 Impulse Start Circuits.	
9. SOFT START AND LOW-VOLTAGE INHIBIT	1.80
9.1 Introduction. 9.2 Soft-Start Circuit. 9.3 Low-Voltage Inhibit. 9.4 Problems.	
10. TURN-ON VOLTAGE OVERSHOOT PREVENTION	1.85
10.1 Introduction. 10.2 Typical Causes of Turn-on Voltage Overshoot in Switchmode Supplies. 10.3 Overshoot Prevention. 10.4 Problems.	

11. OVERVOLTAGE PROTECTION	1.89
11.1 Introduction. 11.2 Types of Overvoltage Protection. 11.3 Type 1, SCR "Crowbar" Overvoltage Protection. 11.4 "Crowbar" Performance. 11.5 Limitations of "Simple" Crowbar Circuits. 11.6 Type 2, Overvoltage Clamping Techniques. 11.7 Overvoltage Clamping With SCR "Crowbar" Backup. 11.8 Selecting Fuses for SCR "Crowbar" Overvoltage Protection Circuits. 11.9 Type 3, Overvoltage Protection by Voltage Limiting Techniques. 11.10 Problems.	
12. UNDERVOLTAGE PROTECTION	1.101
12.1 Introduction. 12.2 Undervoltage Suppressor Performance Parameters. 12.3 Basic Principles. 12.4 Practical Circuit Description. 12.5 Operating Principles (Practical Circuit). 12.6 Transient Behavior. 12.7 Problems.	
13. OVERLOAD PROTECTION	1.107
13.1 Introduction. 13.2 Types of Overload Protection. 13.3 Type 1, Overpower Limiting. 13.4 Type 1, Form A, Primary Overpower Limiting. 13.5 Type 1, Form B, Delayed Overpower Shutdown Protection. 13.6 Type 1, Form C, Pulse-by-Pulse Overpower/Current Limiting. 13.7 Type 1, Form D, Constant Power Limiting. 13.8 Type 1, Form E, Foldback (Reentrant) Overpower Limiting. 13.9 Type 2, Output Constant Current Limiting. 13.10 Type 3, Overload Protection by Fuses, Current Limiting, or Trip Devices. 13.11 Problems.	
14. FOLDBACK (REENTRANT) OUTPUT CURRENT LIMITING	1.113
14.1 Introduction. 14.2 Foldback Principle. 14.3 Foldback Circuit Principles as Applied to a Linear Supply. 14.4 "Lockout" in Foldback Current-Limited Supplies. 14.5 Reentrant Lockout with Cross- Connected Loads. 14.6 Reentrant Current Limits in Switchmode Supplies. 14.7 Problems.	
15. BASE DRIVE REQUIREMENTS FOR HIGH-VOLTAGE BIPOLAR TRANSISTORS	1.121
15.1 Introduction. 15.2 Secondary Breakdown. 15.3 Incorrect Turn- Off Drive Waveforms. 15.4 Correct Turn-Off Waveform. 15.5 Correct Turn-On Waveform. 15.6 Antisaturation Drive Techniques. 15.7 Optimum Drive Circuit for High-Voltage Transistors. 15.8 Problems.	
16. PROPORTIONAL DRIVE CIRCUITS FOR BIPOLAR TRANSISTORS	1.126
16.1 Introduction. 16.2 Example of a Proportional Drive Circuit. 16.3 Turn-On Action. 16.4 Turn-Off Action. 16.5 Drive Transformer Restoration. 16.6 Wide-Range Proportional Drive Circuits. 16.7 Turn-Off Action. 16.8 Turn-On Action. 16.9 Proportional Drive with High-Voltage Transistors. 16.10 Problems.	
17. ANTISATURATION TECHNIQUES FOR HIGH-VOLTAGE TRANSISTORS	1.132
17.1 Introduction. 17.2 Baker Clamp. 17.3 Problems.	

18. SNUBBER NETWORKS	1.134
18.1 Introduction. 18.2 Snubber Circuit (with Load Line Shaping). 18.3 Operating Principles. 18.4 Establishing Snubber Component Values by Empirical Methods. 18.5 Establishing Snubber Component Values by Calculation. 18.6 Turn-Off Dissipation in Transistor. 18.7 Snubber Resistor Values. 18.8 Dissipation in Snubber Resistor. 18.9 Miller Current Effects. 18.10 The Weaving Low-Loss Snubber Diode. 18.11 "Ideal" Drive Circuits for High-Voltage Bipolar Transistors. 18.12 Problems.	
19. CROSS CONDUCTION	1.144
19.1 Introduction. 19.2 Preventing Cross Conduction. 19.3 Cross-Coupled Inhibit. 19.4 Circuit Operation. 19.5 Problems.	
20. OUTPUT FILTERS	1.149
20.1 Introduction. 20.2 Basic Requirements. 20.3 Parasitic Effects in Switchmode Output Filters. 20.4 Two-Stage Filters. 20.5 High-Frequency Choke Example. 20.6 Resonant Filters. 20.7 Resonant Filter Example. 20.8 Common-Mode Noise Filters. 20.9 Selecting Component Values for Output Filters. 20.10 Main Output Inductor Values (Buck Regulators). 20.11 Design Example. 20.12 Output Capacitor Value. 20.13 Problems.	
21. POWER FAILURE WARNING CIRCUITS	1.161
21.1 Introduction. 21.2 Power Failure and Brownout. 21.3 Simple Power Failure Warning Circuits. 21.4 Dynamic Power Failure Warning Circuit. 21.5 Independent Power Failure Warning Module. 21.6 Power Failure Warning in Flyback Converters. 21.7 Fast Power Failure Warning Circuit. 21.8 Problems.	
22. CENTERING (ADJUSTMENT TO CENTER) OF AUXILIARY OUTPUT VOLTAGES ON MULTIPLE-OUTPUT CONVERTERS	1.170
22.1 Introduction. 22.2 Example. 22.3 Saturable Reactor Voltage Adjustment. 22.4 Reactor Design. 22.5 Problems.	
23. AUXILIARY SUPPLY SYSTEMS	1.174
23.1 Introduction. 23.2 60-Hz Line Transformers. 23.3 Auxiliary Converters. 23.4 Operating Principles. 23.5 Stabilized Auxiliary Converters. 23.6 High-Efficiency Auxiliary Supplies. 23.7 Auxiliary Supplies Derived from Main Converter Transformer. 23.8 Problems.	
24. PARALLEL OPERATION OF VOLTAGE: STABILIZED POWER SUPPLIES	1.179
24.1 Introduction. 24.2 Master-Slave Operation. 24.3 Voltage-Controlled Current Sources. 24.4 Forced Current Sharing. 24.5 Parallel Redundant Operation. 24.6 Problems.	

PART 2 DESIGN: THEORY AND PRACTICE

1. MULTIPLE-OUTPUT FLYBACK SWITCHMODE POWER SUPPLIES	2.3
1.1 Introduction. 1.2 Expected Performance. 1.2.1 Output Ripple and Noise. 1.2.2 Synchronization. 1.3 Operating Modes. 1.3.1 Transfer Function. 1.3.2 Current-Mode Control. 1.4 Operating Principles. 1.5 Energy Storage Phase. 1.6 Energy Transfer Modes (Flyback Phase). 1.7 Factors Defining Operating Modes. 1.7.1 Complete Energy Transfer. 1.7.2 Incomplete Energy Transfer. 1.8 Transfer Function Anomaly. 1.9 Transformer Throughput Capability. 1.10 Specification Notes. 1.11 Specification Example for a 110-W Direct-Off-Line Flyback Power Supply. 1.11.1 Specification. 1.11.2 Power Circuit. 1.11.3 Transformer Design. 1.12 Problems.	
2. FLYBACK TRANSFORMER DESIGN	2.16
2.1 Introduction. 2.2 Core Parameters for the Effect of an Air Gap. 2.3 General Design Considerations. 2.4 Design Example for a 110-W Flyback Transformer. 2.5 Flyback Transformer Saturation and Transient Effects. 2.6 Conclusions. 2.7 Problems.	
3. REDUCING TRANSISTOR SWITCHING STRESS	2.32
3.1 Introduction. 3.2 Self-Tracking Voltage Clamp. 3.3 Flyback Converter "Snubber" Networks. 3.4 Problems.	
4. SELECTING POWER COMPONENTS FOR FLYBACK CONVERTERS	2.37
4.1 Introduction. 4.2 Primary Components. 4.3 Secondary Power Components. 4.4 Output Capacitors. 4.5 Capacitor Life. 4.6 General Conclusions Concerning Flyback Converter Components. 4.7 Problems.	
5. THE DIAGONAL HALF-BRIDGE FLYBACK CONVERTER	2.44
5.1 Introduction. 5.2 Operating Principle. 5.3 Useful Properties. 5.4 Transformer Design. 5.5 Drive Circuitry. 5.6 Operating Frequency. 5.7 Snubber Components. 5.8 Problems.	
6. SELF-OSCILLATING DIRECT-OFF-LINE FLYBACK CONVERTERS	2.49
6.1 Introduction. 6.2 Classes of Operation. 6.3 General Operating Principles. 6.4 Isolated Self-Oscillating Flyback Converters. 6.5 Control Circuit (Brief Description). 6.6 Squegging. 6.7 Summary of the Major Parameters for Self-Oscillating Flyback Converters. 6.8 Problems.	
7. APPLYING CURRENT-MODE CONTROL TO FLYBACK CONVERTERS	2.58
7.1 Introduction. 7.2 Power Limiting and Current-Mode Control as Applied to the Self-oscillating Flyback Converter. 7.3 Voltage Control Loop. 7.4 Input Ripple Rejection. 7.5 Using Field-Effect Transistors in Variable-Frequency Flyback Converters. 7.6 Problems.	

8. DIRECT-OFF-LINE SINGLE-ENDED FORWARD CONVERTERS	2.63
<i>8.1 Introduction. 8.2 Operating Principles. 8.3 Limiting Factors for the Value of the Output Choke. 8.4 Multiple Outputs. 8.5 Energy Recovery Winding (P2). 8.6 Advantages. 8.7 Disadvantages. 8.8 Problems.</i>	
9. TRANSFORMER DESIGN FOR FORWARD CONVERTERS	2.70
<i>9.1 Introduction. 9.2 Transformer Design Example. 9.3 Selecting Power Transistors. 9.4 Final Design Notes. 9.5 Transformer Saturation. 9.6 Conclusions.</i>	
10. DIAGONAL HALF-BRIDGE FORWARD CONVERTERS	2.80
<i>10.1 Introduction. 10.2 Operating Principle.</i>	
11. TRANSFORMER DESIGN FOR DIAGONAL HALF-BRIDGE FORWARD CONVERTERS	2.84
<i>11.1 General Considerations. 11.2 Design Notes.</i>	
12. HALF-BRIDGE PUSH-PULL DUTY-RATIO-CONTROLLED CONVERTERS	2.90
<i>12.1 Introduction. 12.2 Operating Principles. 12.3 System Advantages. 12.4 Problem Areas. 12.5 Current-Mode Control and Subharmonic Ripple. 12.6 Cross-Conduction Prevention. 12.7 Snubber Components (Half-Bridge). 12.8 Soft Start. 12.9 Transformer Design. 12.10 Optimum Flux Density. 12.11 Transient Conditions. 12.12 Calculating Primary Turns. 12.13 Calculate Minimum Primary Turns (N_{mpp}). 12.14 Calculate Secondary Turns. 12.15 Control and Drive Circuits. 12.16 Flux Doubling Effect. 12.17 Problems.</i>	
13. BRIDGE CONVERTERS	2.103
<i>13.1 Introduction. 13.2 Operating Principle. 13.3 Transformer Design (Full Bridge). 13.4 Transformer Design Example. 13.5 Staircase Saturation. 13.6 Transient Saturation Effects. 13.7 Forced Flux Density Balancing. 13.8 Problems.</i>	
14. LOW-POWER SELF-OSCILLATING AUXILIARY CONVERTERS	2.116
<i>14.1 Introduction. 14.2 General Operating Principles. 14.3 Operating Principle, Single-Transformer Converters. 14.3 Transformer Design.</i>	
15. SINGLE-TRANSFORMER TWO-TRANSISTOR SELF-OSCILLATING CONVERTERS	2.121
<i>15.1 Introduction. 15.2 Operating Principles (Gain-Limited Switching). 15.3 Defining the Switching Current. 15.4 Choosing Core Materials. 15.5 Transformer Design (Saturating Core Type Converters). 15.6 Problems.</i>	

16. TWO-TRANSFORMER SELF-OSCILLATING CONVERTERS	2.132
<i>16.1 Introduction. 16.2 Operating Principles. 16.3 Saturated Drive Transformer Design. 16.4 Selecting Core Size and Material. 16.5 Main Power Transformer Design. 16.6 Problems.</i>	
17. THE DC-TO-DC TRANSFORMER CONCEPT	2.137
<i>17.1 Introduction. 17.2 Basic Principle of the DC-to-DC Transformer Concept. 17.3 DC-to-DC Transformer Example. 17.4 Problems.</i>	
18. MULTIPLE-OUTPUT COMPOUND REGULATING SYSTEMS	2.141
<i>18.1 Introduction. 18.2 Buck Regulator, Cascaded with a DC-to-DC Transformer. 18.3 Operating Principles. 18.4 Buck Regulator Section. 18.5 DC Transformer Section. 18.6 Synchronized Compound Regulators. 18.7 Compound Regulators with Secondary Post Regulators. 18.8 Problems.</i>	
19. DUTY-RATIO-CONTROLLED PUSH-PULL CONVERTERS	2.147
<i>19.1 Introduction. 19.2 Operating Principles. 19.3 Snubber Components. 19.4 Staircase Saturation in Push-Pull Converters. 19.5 Flux Density Balancing. 19.6 Push-Pull Transformer Design (General Considerations). 19.7 Flux Doubling. 19.8 Push-Pull Transformer Design Example. 19.9 Problems.</i>	
20. DC-TO-DC SWITCHING REGULATORS	2.160
<i>20.1 Introduction. 20.2 Operating Principles. 20.3 Control and Drive Circuits. 20.4 Inductor Design for Switching Regulators. 20.5 Inductor Design Example. 20.6 General Performance Parameters. 20.7 The Ripple Regulator. 20.8 Problems.</i>	
21. HIGH-FREQUENCY SATURABLE REACTOR POWER REGULATORS (Magnetic Duty Ratio Control)	2.174
<i>21.1 Introduction. 21.2 Operating Principles. 21.3 The Saturable Reactor Power Regulator Principle. 21.4 The Saturable Reactor Power Regulator Application. 21.5 Saturable Reactor Quality Factors. 21.6 Selecting Suitable Core Materials. 21.7 Controlling the Saturable Reactor. 21.8 Current Limiting the Saturable Reactor Regulator. 21.9 Push-Pull Saturable Reactor Secondary Power Control Circuit. 21.10 Some Advantages of the Saturable Reactor Regulator. 21.11 Some Limiting Factors in Saturable Reactor Regulators. 21.12 The Case for Constant-Voltage or Constant-Current Reset (High-Frequency Instability Considerations). 21.13 Saturable Reactor Design. 21.14 Design Example. 21.15 Problems.</i>	
22. CONSTANT-CURRENT POWER SUPPLIES	2.191
<i>22.1 Introduction. 22.2 Constant-Voltage Supplies. 22.3 Constant-Current Supplies. 22.4 Compliance Voltage. 22.5 Problems.</i>	

23. VARIABLE LINEAR POWER SUPPLIES	2.195
23.1 Introduction. 23.2 Basic Operation (Power Section). 23.3 Drive Circuit. 23.4 Maximum Transistor Dissipation. 23.5 Distribution of Power Losses. 23.6 Voltage Control and Current Limit Circuit. 23.7 Control Circuit. 23.8 Problems.	
24. SWITCHMODE VARIABLE POWER SUPPLIES	2.204
24.1 Introduction. 24.2 Variable Switchmode Techniques. 24.3 Special Properties of Flyback Converters (in Variable Supplies). 24.4 Operating Principle. 24.5 Practical Limiting Factors. 24.6 Practical Design Compromises. 24.7 Initial Conditions. 24.8 The Diagonal Half Bridge. 24.9 Block Schematic Diagram (General Description). 24.10 Overall System Operating Principles. 24.11 Individual Block Functions. 24.12 Primary Power Limiting. 24.13 Conclusions.	
25. SWITCHMODE VARIABLE POWER SUPPLY TRANSFORMER DESIGN	2.220
25.1 Design Steps. 25.2 Variable-Frequency Mode. 25.3 Problems.	

PART 3 APPLIED DESIGN

1. INDUCTORS AND CHOKES IN SWITCHMODE SUPPLIES	3.3
1.1 Introduction. 1.2 Simple Inductors. 1.3 Common-Mode Line-Filter Inductors. 1.4 Design Example of a Common-Mode Line-Filter Inductor (Using a Ferrite E Core and a Graphical Design Method). 1.5 Calculating Inductance (for Common-Mode Inductors Wound on Ferrite E Cores). 1.6 Series-Mode Line-Input-Filter Inductors. 1.7 Chokes (Inductors with DC Bias). 1.8 Design Example of a Gapped Ferrite E-Core Choke (Using an Empirical Method). 1.9 Design Example of Chokes for Buck and Boost Converters (by Area Product Graphical Methods and by Calculation). 1.10 Choke Design Example for a Buck Regulator (Using a Ferrite E Core and Graphical AP Design Method). 1.11 Ferrite and Iron Powder Rod Chokes. 1.12 Problems.	
2. HIGH-CURRENT CHOKES USING IRON POWDER CORES	3.29
2.1 Introduction. 2.2 Energy Storage Chokes. 2.3 Core Permeability. 2.4 Gapping Iron Powder E Cores. 2.5 Method Used to Design Iron Powder E-Core Chokes (Graphical Area Product Method). 2.6 Example of Iron Powder E-Core Choke Design (Using the Graphical Area Product Method).	
3. CHOKE DESIGN USING IRON POWDER TOROIDAL CORES	3.41
3.1 Introduction. 3.2 Preferred Design Approach (Toroids). 3.3 Swinging Chokes. 3.4 Winding Options. 3.5 Design Example (Option A). 3.6 Design Example (Option B). 3.7 Design Example (Option C). 3.8 Core Loss. 3.9 Total Dissipation and Temperature Rise. 3.9.1 Calculate Winding Resistance. 3.10 Linear (Toroidal) Choke Design.	

Appendix 3A, Derivation of Area Product Equations.	
Appendix 3B, Derivation of Packing and Resistance Factors.	
Appendix 3C, Derivation of Nomogram 3.3.1.	
4. SWITCHMODE TRANSFORMER DESIGN: GENERAL PRINCIPLES	3.64
4.1 Introduction. 4.2 Transformer Size (General Considerations). 4.3 Optimum Efficiency. 4.4 Optimum Core Size and Flux Density Swing. 4.5 Calculating Core Size in Terms of Area Product. 4.6 Winding Packing Factor K_u . 4.7 Primary Area Factor K_p . 4.8 RMS Current Factor K_r . 4.9 The Effect of Frequency on Transformer Size. 4.10 Flux Density Swing ΔB . 4.11 The Impact of Agency Specifications on Transformer Size. 4.12 Calculation of Primary Turns. 4.13 Calculating Secondary Turns. 4.14 Half Turns. 4.15 Wire Sizes. 4.16 Skin Effects and Optimum Wire Thickness. 4.17 Winding Topology. 4.18 Temperature Rise. 4.19 Efficiency. 4.20 Higher Temperature Rise Designs. 4.21 Eliminating Breakdown Stress in Bifilar Windings. 4.22 RFI Screens and Safety Screens. 4.23 Transformer Half-Turn Techniques. 4.24 Transformer Finishing and Vacuum Impregnation. 4.25 Problems.	
Appendix 4A, Derivation of Area Product Equations for Transformer Design.	
Appendix 4B, Skin and Proximity Effects in High-Frequency Transformer Windings.	
5. OPTIMUM 150-W TRANSFORMER DESIGN EXAMPLE USING NOMOGRAMS	3.108
5.1 Introduction. 5.2 Core Size and Optimum Flux Density Swing. 5.2.1 Use of Nomogram 3.4.3. 5.3 Core and Bobbin Parameters. 5.4 Calculate Primary Turns. 5.5 Calculate Primary Wire Size. 5.6 Primary Skin Effects. 5.7 Secondary Turns. 5.8 Secondary Wire Size. 5.9 Secondary Skin Effects. 5.10 Design Notes. 5.11 Design Confirmation. 5.12 Primary Copper Loss. 5.13 Secondary Copper Loss. 5.14 Core Loss. 5.15 Temperature Rise. 5.16 Efficiency.	
6. TRANSFORMER STAIRCASE SATURATION	3.115
6.1 Introduction. 6.2 Methods of Reducing Staircase Saturation Effects. 6.3 Forced Flux Balancing in Duty-Ratio-Controlled Push-Pull Converters. 6.4 Staircase Saturation Problems in Current-Mode Control Systems. 6.5 Problems.	
7. FLUX DOUBLING	3.121
8. STABILITY AND CONTROL-LOOP COMPENSATION IN SMPS	3.122
8.1 Introduction. 8.2 Some Causes of Instability in Switchmode Supplies. 8.3 Methods of Stabilizing the Loop. 8.4 Stability Testing Methods. 8.5 Test Procedure. 8.6 Transient Testing Analysis. 8.7 Bode Plots. 8.8 Measurement Procedures for Bode Plots of Closed-Loop Power Supply Systems. 8.9 Test Equipment for Bode Plot Measurement. 8.10 Test Techniques. 8.11 Measurement Procedures for Bode Plots of Open-Loop Power Supply Systems. 8.12 Establishing Optimum Compensation Characteristic by the "Difference" Method. 8.13 Some Causes of Stubborn Instability. 8.14 Problems.	

9. THE RIGHT-HALF-PLANE ZERO	3.136
9.1 Introduction. 9.2 Explanation of the Dynamics of the Right-Half-Plane Zero. 9.3 The Right-Half-Plane Zero—A Simplified Explanation. 9.4 Problems.	
10. CURRENT-MODE CONTROL	3.142
10.1 Introduction. 10.2 The Principles of Current-Mode Control. 10.3 Converting Current-Mode Control to Voltage Control. 10.4 Performance of the Complete Energy Transfer Current-Mode-Controlled Flyback Converter. 10.5 The Advantages of Current-Mode Control in Continuous-Inductor-Current Converter Topologies. 10.6 Slope Compensation. 10.7 Advantages of Current-Mode Control in Continuous-Inductor-Current-Mode Buck Regulator. 10.8 Disadvantages Intrinsic to Current-Mode Control. 10.9 Flux Balancing in Push-Pull Topologies When Using Current-Mode Control. 10.10 Asymmetry Caused by Charge Imbalance in Current-Mode-Controlled Half-Bridge Converters and Other Topologies Using DC Blocking Capacitors. 10.11 Summary. 10.12 Problems.	
11. OPTOCOUPLERS	3.161
11.1 Introduction. 11.2 Optocoupler Interface Circuit. 11.3 Stability and Noise Sensitivity. 11.4 Problems.	
12. RIPPLE CURRENT RATINGS FOR ELECTROLYTIC CAPACITORS IN SWITCHMODE POWER SUPPLIES	3.166
12.1 Introduction. 12.2 Establishing Capacitor RMS Ripple Current Ratings From Published Data. 12.3 Establishing the Effective RMS Ripple Current in Switchmode Output-Filter Capacitor Applications. 12.4 Recommended Test Procedures. 12.5 Problems.	
13. NONINDUCTIVE CURRENT SHUNTS	3.172
13.1 Introduction. 13.2 Current Shunts. 13.3 Resistance/Inductance Ratio of a Simple Shunt. 13.4 Measurement Error. 13.5 Construction of Low-Inductance Current Shunts. 13.6 Problems.	
14. CURRENT TRANSFORMERS	3.176
14.1 Introduction. 14.2 Types of Current Transformers. 14.3 Core Size and Magnetizing Current (All Types). 14.4 Current Transformer Design Procedure. 14.5 Unidirectional Current Transformer Design Example. 14.6 Type 2, Current Transformers (for Alternating Current Push-Pull Applications). 14.7 Type 3, Flyback-Type Current Transformers. 14.8 Type 4, DC Current Transformers (DCCT). 14.9 Using Current Transformers in Flyback Converters.	
15. CURRENT PROBES FOR MEASUREMENT PURPOSES	3.193
15.1 Introduction. 15.2 Special-Purpose Current Probes. 15.3 The Design of Current Probes for Unidirectional (Discontinuous) Current Pulse Measurements. 15.4 Select Core Size. 15.5 Calculate Required	

Core Area. 15.6 Check Magnetization Current Error. 15.7 Current Probes in Applications with DC and AC Currents. 15.8 High-Frequency AC Current Probes. 15.9 Low-Frequency AC Current Probes. 15.10 Problems.	
16. THERMAL MANAGEMENT (In Switchmode Power Supplies)	3.201
16.1 Introduction. 16.2 The Effect of High Temperatures on Semiconductor Life and Power Supply Failure Rates. 16.3 The Infinite Heat Sink, Heat Exchangers, Thermal Shunts, and Their Electrical Analogs. 16.4 The Thermal Circuit and Equivalent Electrical Analog. 16.5 Heat Capacity C_h (Analogous to Capacitance C). 16.6 Calculating Junction Temperature. 16.7 Calculating the Heat Sink Size. 16.8 Methods of Optimizing Thermal Conductivity Paths, and Where to Use "Thermal Conductive Joint Compound". 16.9 Convection, Radiation, or Conduction? 16.10 Heat Exchanger Efficiency. 16.11 The Effect of Input Power on Thermal Resistance. 16.12 Thermal Resistance and Heat Exchanger Area. 16.13 Forced-Air Cooling. 16.14 Problems.	
Glossary	3.222
References	3.231
Index	

ACKNOWLEDGEMENTS

No man is an island. We progress not only by our own efforts, but also by utilizing the work of those around us and by building on the foundations of those who went before. The reference section is an attempt to acknowledge this, and I have no doubt that many more works should have been mentioned. I sincerely apologize for any omissions; it is often difficult to remember the original source.

I am grateful to the many who have contributed to this work, but worthy of special mention is my former engineering colleague and friend Rodney Weaving, who not only spent many hundreds of hours carefully checking the original manuscript and calculations, but also made many useful suggestions. Without his endorsement I do not think I would have had the courage to put this into print. I am also grateful to Betty Magee, who transcribed my dictation onto computer diskette for further word processing; Hammond Manufacturing Co., Ltd., of Guelph, Ontario, who generously provided illustrating equipment and encouragement; Gordon (Ed) Bloom, president of Bloom Associates, Inc., for his helpful suggestions; Unitrode and Lloyd H. Dixon, Jr., for permission to reproduce his work on "The Right-Half-Plane Zero"; the editors and staff of McGraw-Hill Publishing Company, who added the "professional touch"; and finally Line Donald of Douglas J. Donald (illustration and design), Guelph, Ontario, who produced the more than three hundred camera-ready illustrations and diagrams. Her professional skill has greatly enhanced this presentation.

UNITS, SYMBOLS, DIMENSIONS, AND ABBREVIATIONS USED IN THIS BOOK

Units, Symbols, and Dimensions

In general, the units and symbols used in this book conform to the International Standard (SI) System. However, to yield convenient solutions, the equations are often dimensionally modified to convenient multiples or submultiples. (The preferred dimensions are shown following each equation.)

The imperial system is used for thermal calculations, because most thermal information is still presented in this form. Dimensions are in inches (1 in = 25.4 mm) and temperatures are in degrees Celsius, except for radiant heat calculations, which use the absolute Kelvin temperature scale.

Some graphs and equations in the magnetics sections use CGS units where this is common practice. Many manufacturers still provide magnetic information in CGS units; for example, magnetic field strength is shown in oersted(s) rather than At/m. (1 At/m = 12.57×10^{-3} Oe.)

It is industry standard practice to show core loss in terms of milliwatts per gram, with "peak flux density \hat{B} " as a parameter. (Because these graphs were developed for conventional push-pull transformer applications, symmetrical flux density swing about zero is assumed.) Hence, loss graphs assume a peak-to-peak swing of $2 \times \hat{B}$. To prevent confusion, when nonsymmetrical flux excursions are considered in this book, the term "peak flux density \hat{B} " is used only to indicate peak values. The term "flux density swing ΔB " is used to indicate total peak-to-peak excursion.

Basic Entities

Unit symbol	Unit name	Quantity	Quantity symbol	Dimensions
kg	Kilogram	Mass	<i>m</i>	<i>M</i>
m	Meter	Length	<i>l</i>	<i>L</i>
s	Second	Time	<i>t</i>	<i>T</i>
A	Ampere	Electric current	<i>I</i>	Q/T^{-1}
K	Kelvin	Temperature	<i>T</i>	θ

Multiples and Submultiples of Units Are Limited to the Following Range

Symbol prefix	Prefix name	Power multiple
M	mega-	10^6
k	kilo-	10^3
m	milli-	10^{-3}
μ	micro-	10^{-6}
n	nano-	10^{-9}
p	pico-	10^{-12}

Symbols for Physical Quantities

Quantity	Quantity symbol	Unit name	Unit symbol	Formula
<i>Electric</i>				
Capacitance	<i>C</i>	farads	F	$S \cdot s$
Charge	<i>Q</i>	coulombs	C	$A \cdot s$
Current	<i>I</i>	amperes	A	V/Ω
Energy	<i>U</i>	joules	J	$W \cdot s$
Impedance	<i>Z</i>	ohms	Ω	—
Inductance, self-	<i>L</i>	henries	H	Wb/A
Potential difference	<i>V</i>	volts	V	Wb/s
Power, real (active)	<i>P</i>	watts	W	$VI \cos \theta$
power, apparent	<i>S</i>	voltamperes	VA	$V \cdot A$
Reactance	<i>X</i>	ohms	Ω	—
Resistance	<i>R</i>	ohms	Ω	V/A
Resistivity, volume	ρ	ohm-centimeters cubed	$\Omega\text{-cm}^3$	$\frac{R \cdot A}{l}$
<i>Magnetic</i>				
Field strength	<i>H</i>	amperes per meter	A/m	—
Field strength (CGS)	<i>H</i>	oersteds	Oe	$4\pi 10^{-3} A/M$
Flux	Φ	webers	Wb	$V \cdot s$
Flux density	<i>B</i>	teslas	T	Wb/m
Permeability	μ	henries per meter	H/m	$V \cdot s/A \cdot m$
<i>Other</i>				
Angular velocity	ω	radians per second	rad/s	$2\pi f$
Area	<i>A</i>	centimeters squared	cm^2	—
Frequency	<i>f</i>	hertz	Hz	cycles/s
Length	<i>l</i>	centimeters	cm	—
Skin thickness	Δ	millimeters	mm	—
Temperature	<i>T</i>	degrees Celsius	$^{\circ}\text{C}$	—
Temperature, absolute	<i>T</i>	kelvins	K	—
Time	<i>t</i>	seconds	s	—
Winding height	φ	millimeters	mm	—

Symbols for Mathematical Variables Used in This Book

Variable	Parameter	Unit
<i>A</i>	area	cm^2
<i>A</i>	gain (without feedback)	dB
<i>A'</i>	gain (with feedback)	dB
<i>A_c</i>	minimum cross-sectional area of pole (transformer)	cm^2
<i>A_{cp}</i>	area of center pole (of core)	cm^2
<i>ac</i>	alternating current	A
<i>A_e</i>	effective area (of core)	cm^2
<i>A_g</i>	area of air gap (in core)	cm^2
<i>A_L</i>	inductance factor (inductance of a single turn)	nH
<i>A_m</i>	minimum area of core	cm^2
<i>A_n</i>	attenuation factor	—
<i>A_p</i>	area of center pole (of core)	cm^2
<i>A_{p'}</i>	area of primary winding	cm^2
<i>AP</i>	area product of core ($A_w \times A_e$)	cm^4
<i>A_{pe}</i>	effective area product ($A_{wb} \times A_e$)	cm^4
<i>A_r</i>	resistance factor (bobbin); also attenuation factor	—
<i>A_w</i>	winding window area (of core)	cm^2
<i>A_{wb}</i>	winding window area (of bobbin)	cm^2
<i>A_{wc}</i>	effective area of copper in winding (total)	cm^2
<i>A_{wp}</i>	primary winding window area	cm^2
<i>A_s</i>	surface area	cm^2
<i>A_x</i>	area of copper (for a single wire)	cm^2
<i>B</i>	magnetic flux density	mT
\hat{B}	peak magnetic flux density	mT
<i>B</i>	feedback factor	—
ΔB	small change in <i>B</i>	mT
ΔB_{ac}	magnetic flux density swing (p-p)	mT
<i>B_{dc}</i>	steady-state magnetic flux density (due to H_{dc})	mT
<i>B_{opt}</i>	optimum flux density swing (for minimum loss)	mT
<i>B_r</i>	remanence flux density	mT
<i>B_s</i>	saturation flux density	mT
<i>B_w</i>	peak (working) value of flux density	mT
<i>b_w</i>	useful winding width (of bobbin)	mm
<i>C</i>	capacitance	μF
<i>C_c</i>	leakage (parasitic) capacitance	pF
<i>cfm</i>	cubic feet per minute (of air flow)	cfm
<i>C_h</i>	heat (storage) capacity (joules/in ³ /°C)	Ws/°C
<i>C_k</i>	interelectrode capacitance	pF
<i>C_p</i>	parasitic coupling capacitance	pF
<i>D</i>	duty ratio (t_{on}/t_p)	—
<i>d'</i>	duty cycle (t_{on}/t_{off})	—
<i>D'</i>	$D'(1 - D)$ = "off" time	—
<i>dB</i>	logarithmic ratio (voltage $20 \log_{10} V_1/V_2$ or power $10 \log_{10} P_1/P_2$)	dB
<i>dB_m</i>	logarithmic power ratio with respect to 1 mW ($10 \log_{10} P_1/1 \text{ mW}$)	dB
<i>DC</i>	direct (nonvarying) current or voltage	A or V
<i>di/dt</i>	rate of change of current with respect to time	A/s
<i>di_p/dt</i>	rate of change of primary current with respect to time	A/s
<i>di_s/dt</i>	rate of change of secondary current with respect to time	A/s

Symbols for Mathematical Variables Used in This Book (cont.)

Variable	Parameter	Unit
dv/dt	rate of change of voltage with respect to time	V/s
d_w	wire diameter	mm
e	emf, induced electromotive force (vector quantity)	V
e'	radiant emissivity of surface	
$ e $	emf (magnitude of emf only)	V
E	electrical energy	J
f	frequency	Hz
F_1	layer factor (copper)	
F_r	ratio of ac/DC resistance (of winding)	
H	magnetic field strength	Oe
\hat{H}	peak value of effective magnetic field strength	Oe
h	conductor thickness (strip) or wire diameter	mm
H_{ac}	magnetic field strength swing, p-p	Oe
H_{dc}	magnetic field strength due to DC current	Oe
H_{opt}	optimum value of magnetic field strength	Oe
H_s	saturating value of magnetic field strength	Oe
ΔH	small change in magnetic field strength	Oe
I	current flow (DC)	A
i	rms current (ac)	A
\hat{i}	peak current	A
I_a	current density (in wire)	A/cm ²
I_{ave}	average value of current for a defined period	A
I_{cp}	peak collector current	A
I_{dc}	direct current (dependent variable)	A
I_e	effective input current	A
I_i	harmonic interference current	A
I_L	inductor or choke current (average)	A
i_L	ac inductor current	A
$I_{L(p-p)}$	ripple current p-p in choke or inductor	A
I_{max}	maximum value of current	A
I_{mean}	time-averaged current value	A
I_{min}	minimum value of current	A
I_p	primary current (in transformer)	A
I_s	secondary current (also snubber current)	A
ΔI	small change in current	A
I^2R	resistive power loss	W
j_{wc}	capacitive reactance, $1/2 \pi fC$ (complex #)	Ω
j_{wl}	inductive reactance, $2 \pi fL$ (complex #)	Ω
K'	copper utilization factor (topology factor)	
K_m	material constant	
K_p	primary area factor	
K_t	primary rms current factor	
K_u	packing factor (of wire)	%
K_{ub}	utilization factor of bobbin	
L	inductance (self-inductance of wound component)	H
l	length (length of magnetic path)	cm
l_e	effective path length	cm

Symbols for Mathematical Variables Used in This Book (cont.)

Variable	Parameter	Unit
l_g	total length of air gap (in core)	cm
L_{LP}	primary leakage inductance	μ H
L_{Ls}	secondary leakage inductance	μ H
L_{LT}	total (transformer) leakage inductance	μ H
l_m	mean length of wire or magnetic path (or core)	cm
L_p	primary inductance	mH
L_s	secondary inductance	mH
M_{lt}	mean length per turn	cm
mmf	magnetomotive force (magnetic potential ampere-turns)	At
N	number of turns	
N_{fb}	number of turns of feedback winding	
N_{min}	minimum number of turns (to prevent core saturation)	
N_{mpp}	minimum primary turns for p-p operation	
N_p	primary turns (of transformer)	
N_s	secondary turns (of transformer)	
N_v	turns per volt (on transformer)	T/V
N_w	number of turns (or wires) per layer	
P	power	W
p	period (of time)	μ s
P_c	power dissipated in core	W
P_f	power factor (ratio true power/VA)	—
P_{in}	input power	W
P_{id}	total internal dissipation	W
P_{out}	output power	W
P_{q1}	power dissipated in transistor Q1	W
P_q	heat energy (joules)	J
P_t	total internal dissipation	W
P_v/N	primary volts per turn	V/T
P_w	winding copper loss	W
Q	rate of heat flow (in watts by conduction or in J/s/in ² by radiation)	W J/s
R	resistance	Ω
r	radius (or wire)	mm
R_{Cu}	DC resistance of wound component at specified temperature	Ω
R_e	effective DC resistance of transformer winding	Ω
R_{c-h}	thermal resistance, case to heat exchanger	$^{\circ}$ C/W
R_{h-a}	thermal resistance, heat exchanger to free air	$^{\circ}$ C/W
R_{ha}	thermal resistance of heat exchanger	$^{\circ}$ C/W
R_{j-c}	thermal resistance, junction to case	$^{\circ}$ C/W
rms	square root of the mean of the square of all the harmonic components	V or I
R_o	total thermal resistance	$^{\circ}$ C/W
R_s	effective resistance of prime source or network	Ω
R_{sf}	effective source resistance factor ($R_{sf} = R_s \times W_{out}$)	Ω
RT	temperature coefficient of resistance (copper = 0.00393 at 0 $^{\circ}$ C)	$\Omega/\Omega/^{\circ}$ C
RT_{cm}	resistance of wire in Ω /cm at temp T , $^{\circ}$ C	Ω /cm
R_{θ}	thermal resistance (of heat-conducting path)	$^{\circ}$ C/W
$R_{\theta ja}$	thermal resistance, hot spot to free air	$^{\circ}$ C/W

Symbols for Mathematical Variables Used in This Book (cont.)

Variable	Parameter	Unit
R_{th}	thermal resistance	$^{\circ}\text{C}/\text{W}$
R_w	effective resistance of wound component at frequency f	Ω
R_x	resistance factor of bobbin	
S_f	scaling factor	
T	temperature in degrees Celsius	$^{\circ}\text{C}$
t	time	s
T_{amb}	ambient temperature (of air)	$^{\circ}\text{C}$
T_c	temperature of copper (winding)	$^{\circ}\text{C}$
t_d	time delay period	s
T_{ds}	temperature of surface (diode)	$^{\circ}\text{C}$
t_f	fall time (time required for voltage or current decay)	μs
T_h	temperature of heat exchanger surface	$^{\circ}\text{C}$
t_p	total period (of time), i.e., duration of single cycle	μs
t_{off}	nonconducting "off" time period	μs
t_{on}	conducting "on" time period	μs
ΔT	small change in temperature	$^{\circ}\text{C}$
ΔT_a	small temperature rise (above ambient)	$^{\circ}\text{C}$
Δt	small increment of time	μs
T_r	temperature rise (above ambient)	$^{\circ}\text{C}$
VA	volt-ampere product (apparent power)	VA
V_c	transistor collector voltage	V
V_{cc}	supply line (voltage)	V
V_{ce}	voltage, collector to emitter	V
V_{ceo}	collector-to-emitter breakdown voltage (base open circuit)	V
V_{cer}	collector-to-emitter breakdown voltage (with specified base-to-emitter resistance)	V
V_{cer}	collector-to-emitter breakdown voltage (base reverse-biased)	V
V_e	effective volume of core	cm^3
V_{fb}	feedback voltage	V
V_h	header voltage (voltage at input of regulator)	V
V_{hi}	harmonic interference voltage, rms	Vrms
V_{in}	input voltage	V
V_i	voltage across inductor	V
V_m	mean voltage	V
V_n	nominal (average normal) voltage	V
V/N	volts per turn	V/T
V_o	ripple voltage	V
V_{out}	output voltage	V
V_p	peak voltage or primary voltage	V
V_{p-p}	ripple voltage, peak-peak value	V
V_{ref}	reference voltage	V
V_{rms}	root mean square voltage	Vrms
V_{sat}	saturation voltage	V
W_{in}	true input power ($VI \cos \theta$, or $VA \times P_f$, heating effect)	W
W_{out}	true output power ($VI \cos \theta$, or $VA \times P_f$, heating effect)	W
W_j	heat dissipation at junction, J/s	W
X_c	capacitive reactance ($1/2 \pi fC$)	Ω
X_L	inductive reactance ($2 \pi fL$)	Ω

Symbols for Mathematical Variables Used in This Book (cont.)

Variable	Parameter	Unit
ρ	volume resistivity of copper (at $0^{\circ}\text{C} = 1.588 \mu\Omega/\text{cm}^3$)	$\mu\Omega\text{-cm}^3$
ρ_{tc}	resistivity of copper at t_c $^{\circ}\text{C}$ ($R_{cu} = \frac{\rho_{tc} \cdot l}{A}$)	$\mu\Omega\text{-cm}^3$
μ_0	magnetic field constant ($4 \pi \times 10^{-7} \text{ H/m}$)	Vs/Am
μ_r	relative permeability (of core)	
μ_x	effective permeability (after gap is introduced)	
η	efficiency (power output/power input $\times 100\%$)	%
Δ	a small increment (change); also skin thickness, mm	mm
$\Delta\Phi$	a small change in total flux	Φ
φ	effective conductor height	mm
Φ	total magnetic flux, Wb	Vs
\approx	approximately equal to	
α	proportional to	
ω	angular velocity ($\omega = 2 \pi f$)	rad/s
0V	zero voltage reference line (often the common output)	V
1-D	1 - duty ratio (the "off" period)	s
π	physical constant (3.1416)	
$ x $	magnitude of function (x) only	

ABBREVIATIONS

ac	alternating current
AIEE	American Institute of Electrical Engineers
AWG	American wire gauge
B/H	(curve) hysteresis loop of magnetic material.
CISPR	Comité International Spécial des Perturbations Radioélectriques
CSA	Canadian Standards Association
dB	decibels (logarithmic ratio of power or voltage)
DC	direct (nonvarying) current or voltage
DCCT	direct-current current transformers
e.g.	exemplia gratis
emf	electromotive force
EMI	electromagnetic interference
ESL	effective series inductance
ESR	effective series resistance
FCC	Federal Communications Commission
FET	field-effect transistor
HCR	heavily cold-reduced
HRC	high rupture capacity
IEC	International Electrotechnical Commission
IEEE	Institute of Electrical and Electronics Engineers
LC	(filter) a low-pass filter consisting of a series inductor and shunt capacitor
LED	light-emitting diode
LISN	line impedance stabilization network
mmf	magnetomotive force (magnetic potential, ampere-turns)
MLT	mean length (of wire) per turn

MOV	metal oxide varistor
MPP	molybdenum Permalloy powder
MTBF	mean time before/between failure(s)
NTC	negative temperature coefficient
"on"	conducting (working) state of device (circuit)
"off"	nonconducting (nonworking) state of device (circuit)
OVP	overvoltage protection (circuit)
PARD	periodic and random deviations (see glossary)
pcb	printed circuit board
PFS	power failure sense/signal
p-p	peak-to-peak value (ripple voltage/current)
PTFE	polytetrafluoroethylene
PVC	polyvinyl chloride
PWM	pulse-width modulation
RF	radio frequency
RFI	radio-frequency interference
rms	root mean square
RHP	right-half-plane (zero), a zero located in the right half of the complex s -plane
+s	positive remote sensing (terminal, line)
-s	negative remote sensing (terminal, line)
SCR	silicon controlled rectifier
SMPS	switchmode power supply
SOA	safe operating area
SR	saturable reactor (see glossary)
TTL	transistor-transistor logic
UL	Underwriters' Laboratories
UPS	uninterruptible power supply
UVP	undervoltage protection (circuit)
VDE	Verband Deutscher Elektrotechniker

SWITCHMODE POWER SUPPLY HANDBOOK

P • A • R • T • 1

**FUNCTIONS AND
REQUIREMENTS
COMMON TO MOST
DIRECT-OFF-LINE
SWITCHMODE POWER
SUPPLIES**

CHAPTER 1

COMMON REQUIREMENTS: AN OVERVIEW

1.1 INTRODUCTION

The "direct-off-line" switchmode supply is so called because it takes its power input directly from the ac power lines, without using the rather large low-frequency (60 to 50 Hz) isolation transformer normally found in linear power supplies.

Although the various switchmode conversion techniques are often very different in terms of circuit design, they have, over many years, developed very similar basic functional characteristics which have become generally accepted industry standards.

Further, the need to satisfy various national and international safety, electromagnetic compatibility, and line transient requirements has forced the adoption of relatively standard techniques for track and component spacing, noise filter design, and transient protection. The prudent designer will be familiar with all these agency needs before proceeding with a design. Many otherwise sound designs have failed as a result of their inability to satisfy safety agency standards.

Many of the requirements outlined in this section will be common to all switching supplies, irrespective of the design strategy or circuit. Although the functions tend to remain the same for all units, the circuit techniques used to obtain them may be quite different. There are many ways of meeting these needs, and there will usually be a best approach for a particular application.

The designer must also consider all the minor facets of the specification before deciding on a design strategy. Failure to consider at an early stage some very minor system requirement could completely negate a design approach—for example, power good and power failure indicators and signals, which require an auxiliary supply irrespective of the converter action, would completely negate a design approach which does not provide this auxiliary supply when the converter is inhibited! It can often prove to be very difficult to provide for some minor neglected need at the end of the design and development exercise.

The remainder of Chap. 1 gives an overview of the basic input and output functions most often required by the user or specified by national or international standards. They will assist in the checking or development of the initial specification, and all should be considered before moving to the design stage.

1.2 INPUT TRANSIENT VOLTAGE PROTECTION

Both artificial and naturally occurring electrical phenomena cause very large transient voltages on all but fully conditioned supply lines from time to time.

IEEE Standard 587-1980 shows the results of an investigation of this phenomenon at various locations. These are classified as low-stress class A, medium-stress class B, and high-stress class C locations. Most power supplies will be in low- and medium-risk locations, where stress levels may reach 6000 V at up to 3000 A.

Power supplies are often required to protect themselves and the end equipment from these stress conditions. To meet this need requires special protection devices. (See Part 1, Chap. 2.)

1.3 ELECTROMAGNETIC COMPATIBILITY

Input Filters

Switching power supplies are electrically noisy, and to meet the requirements of the various national and international RFI (radio-frequency interference) regulations for conducted-mode noise, a differential- and common-mode noise filter is normally fitted in series with the line inputs. The attenuation factor required from this noise filter depends on the power supply size, operating frequency, power supply design, application, and environment.

For domestic and office equipments, such as personal computers, VDUs, and so on, the more stringent regulations apply, and FCC class B or similar limits would normally be applied. For industrial applications, the less severe FCC class A or similar limits would apply. (See Part 1, Chap. 3.)

It is important to appreciate that it is very difficult to cure a badly designed supply by fitting filters. The need for minimum noise coupling must be considered at all stages of the design; some good guidelines are covered in Part 1, Chaps. 3 and 4.

1.4 DIFFERENTIAL-MODE NOISE

Differential-mode noise refers to the component of high-frequency electrical noise between any two supply or output lines. For example, this would be measured between the live and neutral input lines or between the positive and negative output lines.

1.5 COMMON-MODE NOISE

For the line input, common-mode noise refers to that component of electrical noise that exists between both supply lines (in common) and the earth (ground) return.

For the outputs, the position is more complicated, as various configurations of

isolated and nonisolated connections are possible. In general, output common-mode noise refers to the electrical noise between any output and some common point, usually the chassis or common return line.

Some specifications, notably those applying to medical electronics, severely limit the amount of ground return current permitted between either supply line and the earth (ground) return. A ground return current normally flows through the filter capacitors and leakage capacitance to ground, even if the insulation is perfect. The return current limitation can have a significant effect on the design of the supply and the size of input filter capacitors. In any event, capacitors in excess of 0.01 μF between the live line and ground are not permitted by many safety standards.

1.6 FARADAY SCREENS

High-frequency conducted-mode noise (noise conducted along the supply or output leads) is normally caused by capacitively coupled currents in the ground plane or between input and output circuits. For this reason, high-voltage switching devices should not be mounted on the chassis. Where this cannot be avoided, a Faraday screen should be fitted between the noise source and the ground plane, or at least the capacitance to the chassis should be minimized.

To reduce input-to-output noise coupling in isolating transformers, Faraday screens should be fitted. These should not be confused with the more familiar safety screens. (See Part 1, Chap. 4.)

1.7 INPUT FUSE SELECTION

This is an often neglected part of power supply design. Modern fuse technology makes available a wide range of fuses designed to satisfy closely defined parameters. Voltages, inrush currents, continuous currents, and let-through energy (I^2t ratings) should all be considered. (See Part 1, Chap. 5.)

Where units are dual-input-voltage-rated, it may be necessary to use a lower fuse rating for the higher input voltage condition. Standard, medium-speed glass cartridge fuses are universally available and are best used where possible. For line input applications, the current rating should take into account the 0.6 to 0.7 power factor of the capacitive input filter used in most switchmode systems.

For best protection the input fuse should have the minimum rating that will reliably sustain the inrush current and maximum operating currents of the supply at minimum line inputs. However, it should be noted that the rated fuse current given in the fuse manufacturer's data is for a limited service life, typically a thousand hours operation. For long fuse life, the normal power supply current should be well below the maximum fuse rating; the larger the margin, the longer the fuse life.

Fuse selection is therefore a compromise between long life and full protection. Users should be aware that fuses tend to age and should be replaced at routine servicing periods. For maximum safety during fuse replacement, the live input is normally fused at a point after the input switch.

To satisfy safety agency requirements and maintain maximum protection, when fuses are replaced, a fuse of the same type and rating must be used.

1.8 LINE RECTIFICATION AND CAPACITOR INPUT FILTERS

Rectifier capacitor input filters have become almost universal for direct-off-line switchmode power supplies. In such systems the line input is directly rectified into a large electrolytic reservoir capacitor.

Although this circuit is small, efficient, and low-cost, it has the disadvantage of demanding short, high-current pulses at the peak of the applied sine-wave input, causing excessive line I^2R losses, harmonic distortion, and a low power factor.

In some applications (e.g., shipboard equipment), this current distortion cannot be tolerated, and special low-distortion input circuits must be used. (See Part 1, Chap. 6.)

1.9 INRUSH LIMITING

Inrush limiting reduces the current flowing into the input terminals when the supply is first switched on. It should not be confused with "soft start," which is a separate function controlling the way the power converter starts its switching action.

In the interests of minimum size and weight, most switchmode supplies will use semiconductor rectifiers and low-impedance input electrolytics in a capacitive input filter configuration. Such systems have an inherently low input resistance; also, because the capacitors are initially discharged, very large surge currents would occur at switch-on if such filters were switched directly to the line input.

Hence, it is normal practice to provide some form of current inrush limiting on power supplies that have capacitive input filters. This inrush limiting typically takes the form of a resistive limiting device in series with the supply lines. In high-power systems, the limiting resistance would normally be removed (shorted out) by an SCR, triac, or switch when the input reservoir and/or filter capacitor has been fully charged. In low-power systems, NTC thermistors are often used as limiting devices.

The selection of the inrush-limiting resistance value is usually a compromise between acceptable inrush current amplitude and start-up delay time. Negative temperature coefficient thermistors are often used in low-power applications, but it should be noted that thermistors will not always give full inrush limiting. For example, if, after the power supply has been running long enough for the thermistor to heat up, the input is turned rapidly off and back on again, the thermistor will still be hot and hence low-resistance, and the inrush current will be large. The published specification should reflect this effect, as it is up to the user to decide whether this limitation will cause any operational problems. Since even with a hot NTC the inrush current will not normally be damaging to the supply, thermistors are usually acceptable and are often used for low-power applications. (See Part 1, Chap. 7.)

1.10 START-UP METHODS

In direct-off-line switchmode supplies, the elimination of the low-frequency (50 to 60 Hz) transformer can present problems with system start-up. The difficulty

usually stems from the fact that the high-frequency power transformer cannot be used for auxiliary supplies until the converter has started. Suitable start-up circuits are discussed in Part 1, Chap. 8.

1.11 SOFT START

Soft start is the term used to describe a low-stress start-up action, normally applied to the pulse-width-modulated converter to reduce transformer and output capacitor stress and to reduce the surge on the input circuits when the converter action starts.

Ideally, the input reservoir capacitors should be fully charged before converter action commences; hence, the converter start-up should be delayed for several line cycles, then start with a narrow pulse and a progressively increasing pulse width until the output is established.

There are, in fact, a number of reasons why the pulse width should be narrow when the converter starts and progressively increase during the start-up phase. There will often be considerable capacitance on the output lines, and this should be charged slowly so that it does not reflect an excessive transient back to the supply lines. Further, where a push-pull action is applied to the main transformer, flux doubling and possible saturation of the core may occur if a wide pulse is applied to the transformer for the first half cycle of operation. (See Part 3, Chap. 7.) Finally, since an inductor will invariably appear somewhere in series with the current path, it may be impossible to prevent voltage overshoot on the output if this inductor current is allowed to rise to a high value during the start-up phase. (See Part 1, Chap. 10.)

1.12 START-UP OVERVOLTAGE PREVENTION

When the power supply is first switched on, the control and regulator circuits are not in their normal working condition (unless they were previously energized by some auxiliary supply).

As a result of the limited output range of the control and driver circuits, the large-signal slew rate may be very nonlinear and slow. Hence, during the start-up phase, a "race" condition can exist between the establishment of the output voltages and correct operation of the control circuits. This can result in excessive output voltage overshoot.

Additional fast-acting voltage clamping circuits may be required to prevent overshoot during the start-up phase, a need often overlooked in the past by designers of both discrete and integrated control circuits. (See Part 1, Chap. 10.)

1.13 OUTPUT OVERVOLTAGE PROTECTION

Loss of voltage control can result in excessive output voltages in both linear and switchmode supplies. In the linear supply (and some switching regulators), there is a direct DC link between input and output circuits, so that a short circuit of the power control device results in a large and uncontrolled output. Such circuits